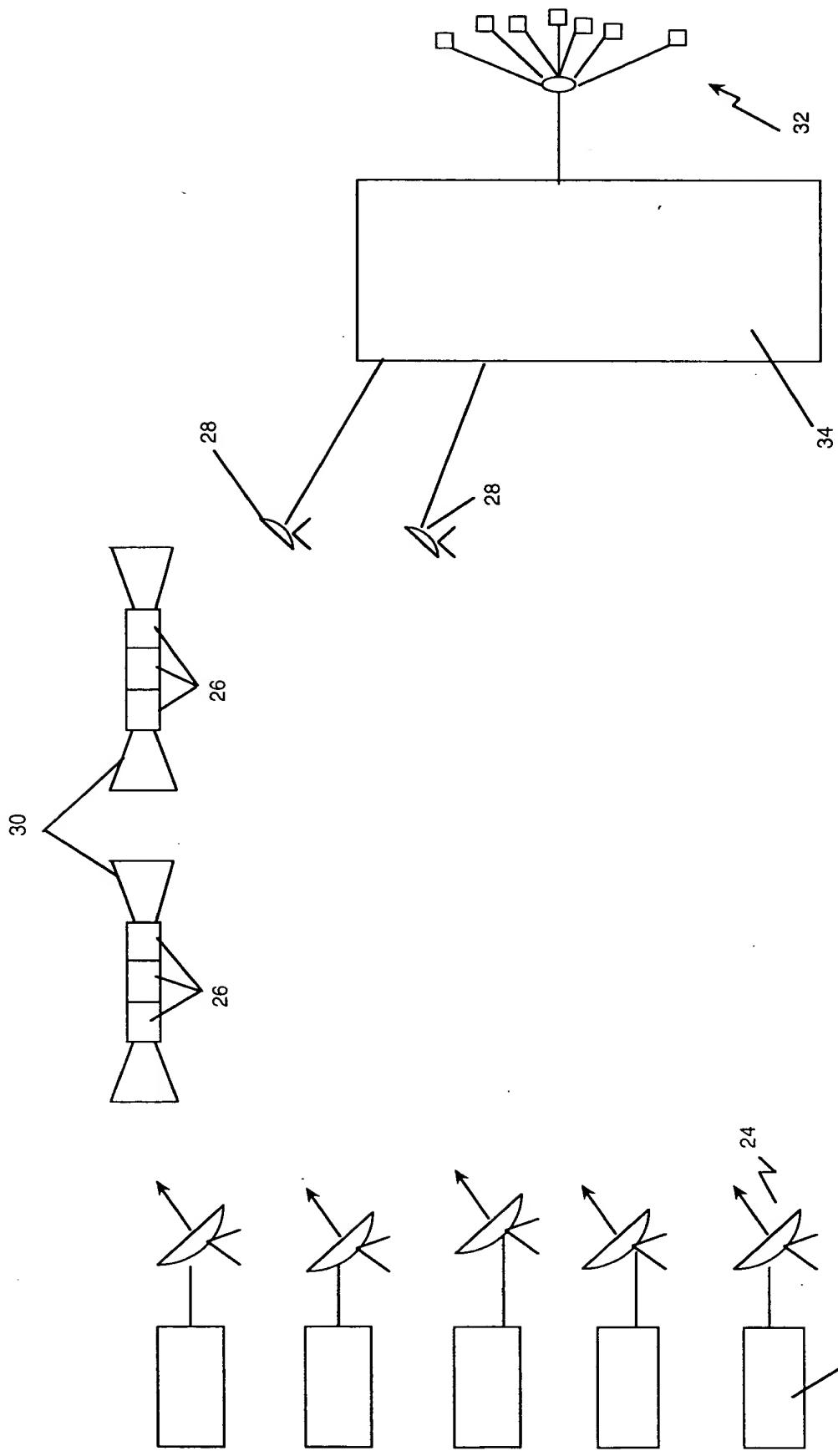


*Fig. 1* Prior Art



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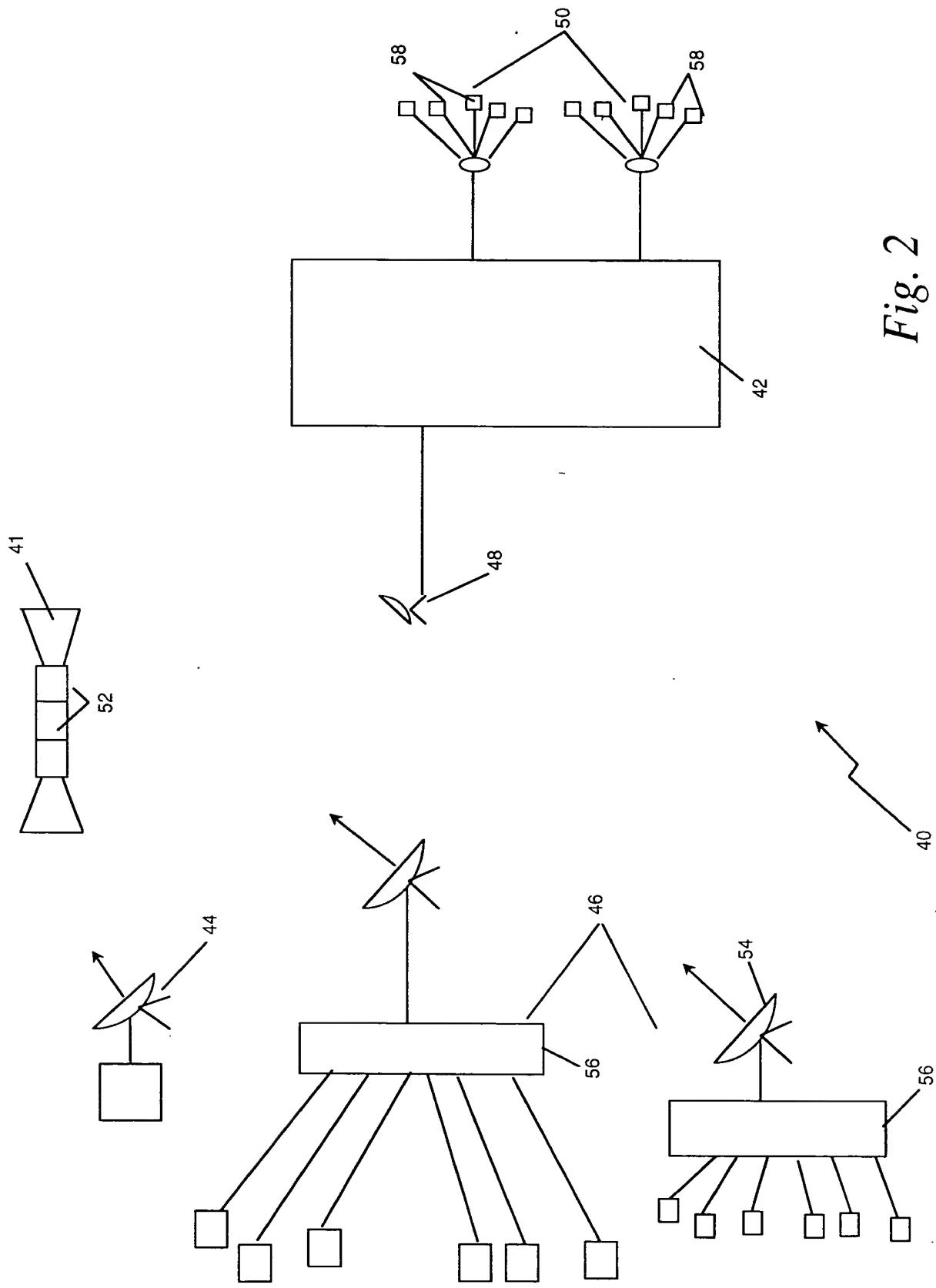
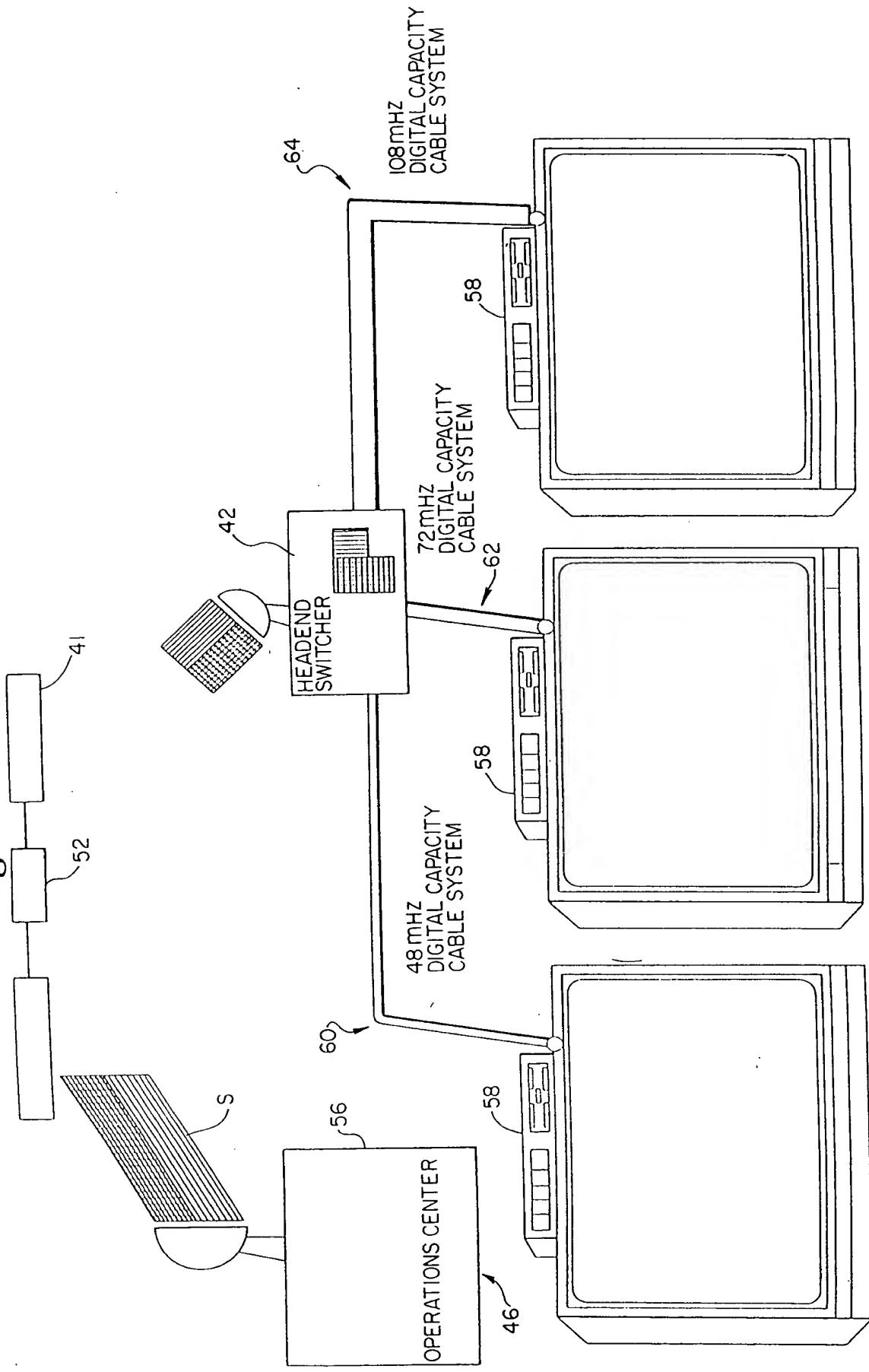
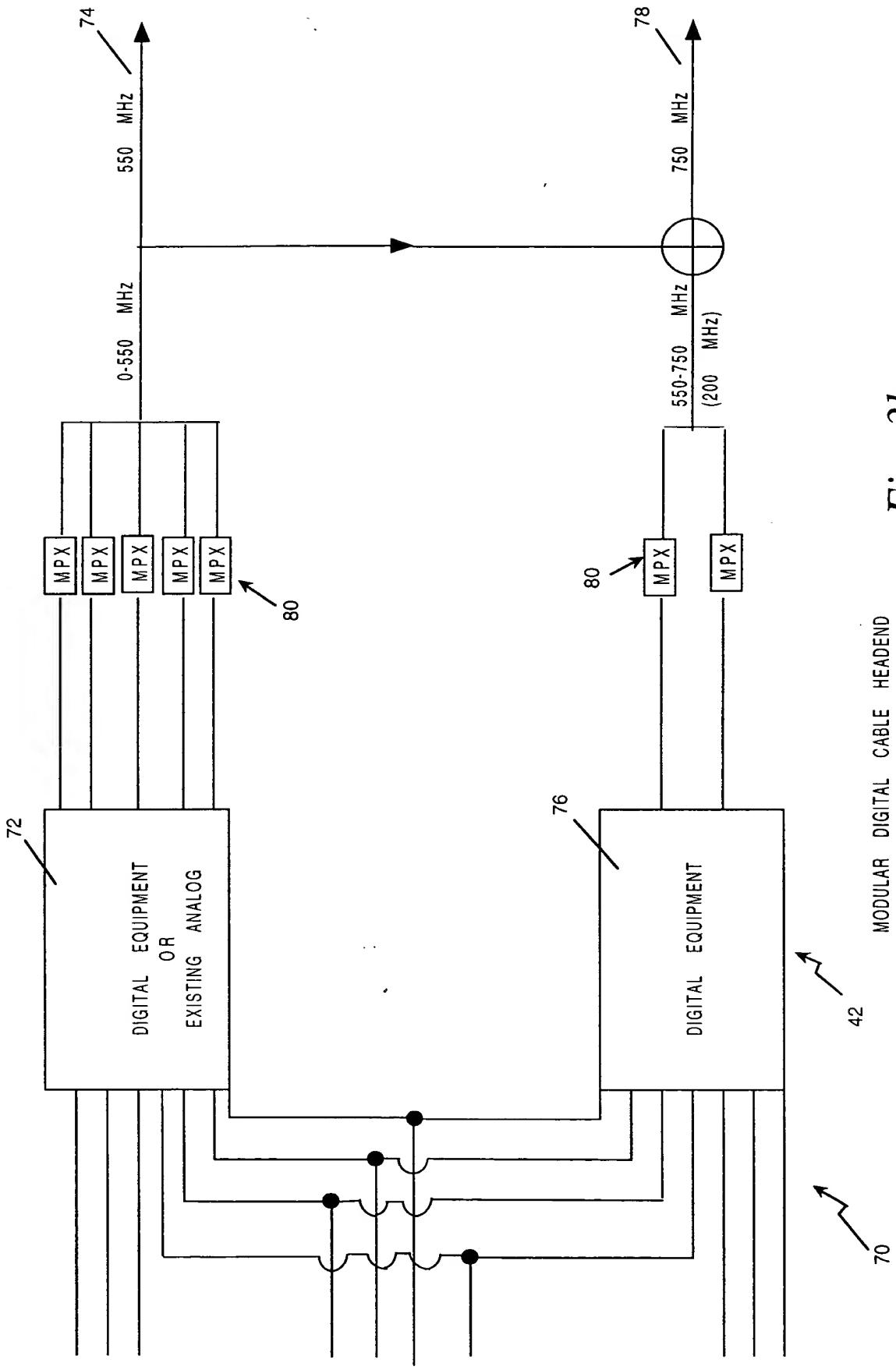


Fig. 2

*Fig. 3a*



*Fig. 3b*



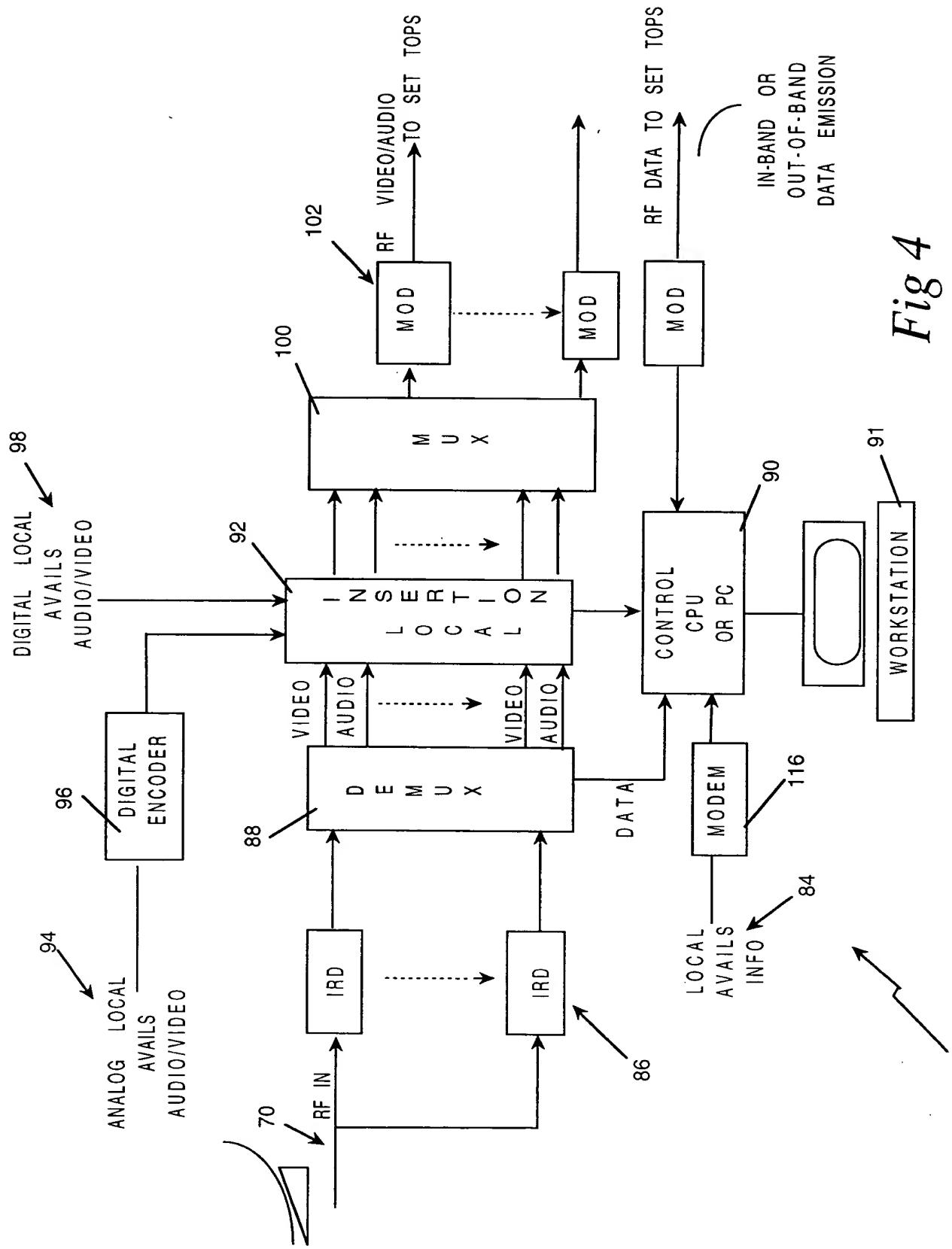


Fig 4

C A B L S Y S T E M

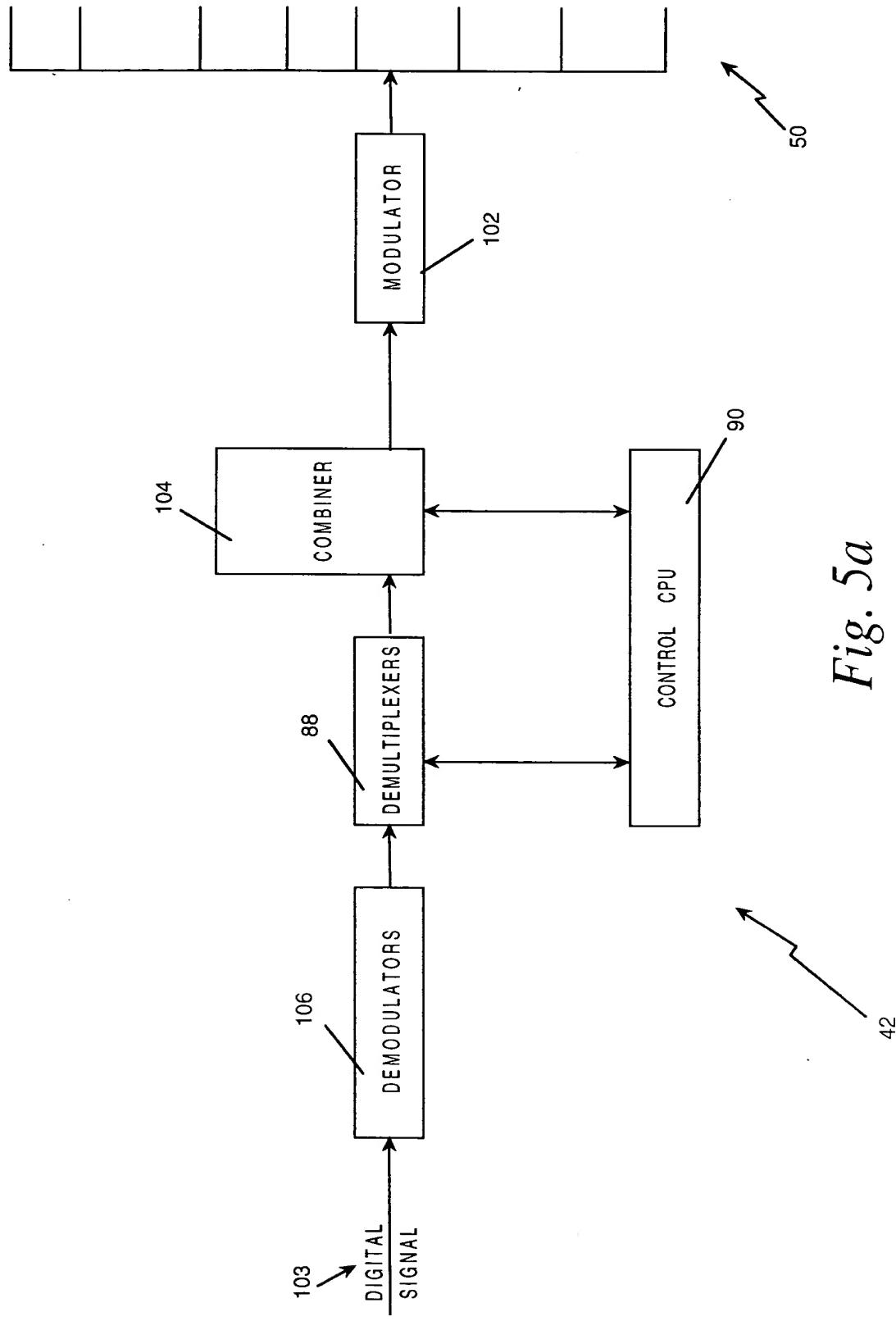


Fig. 5a

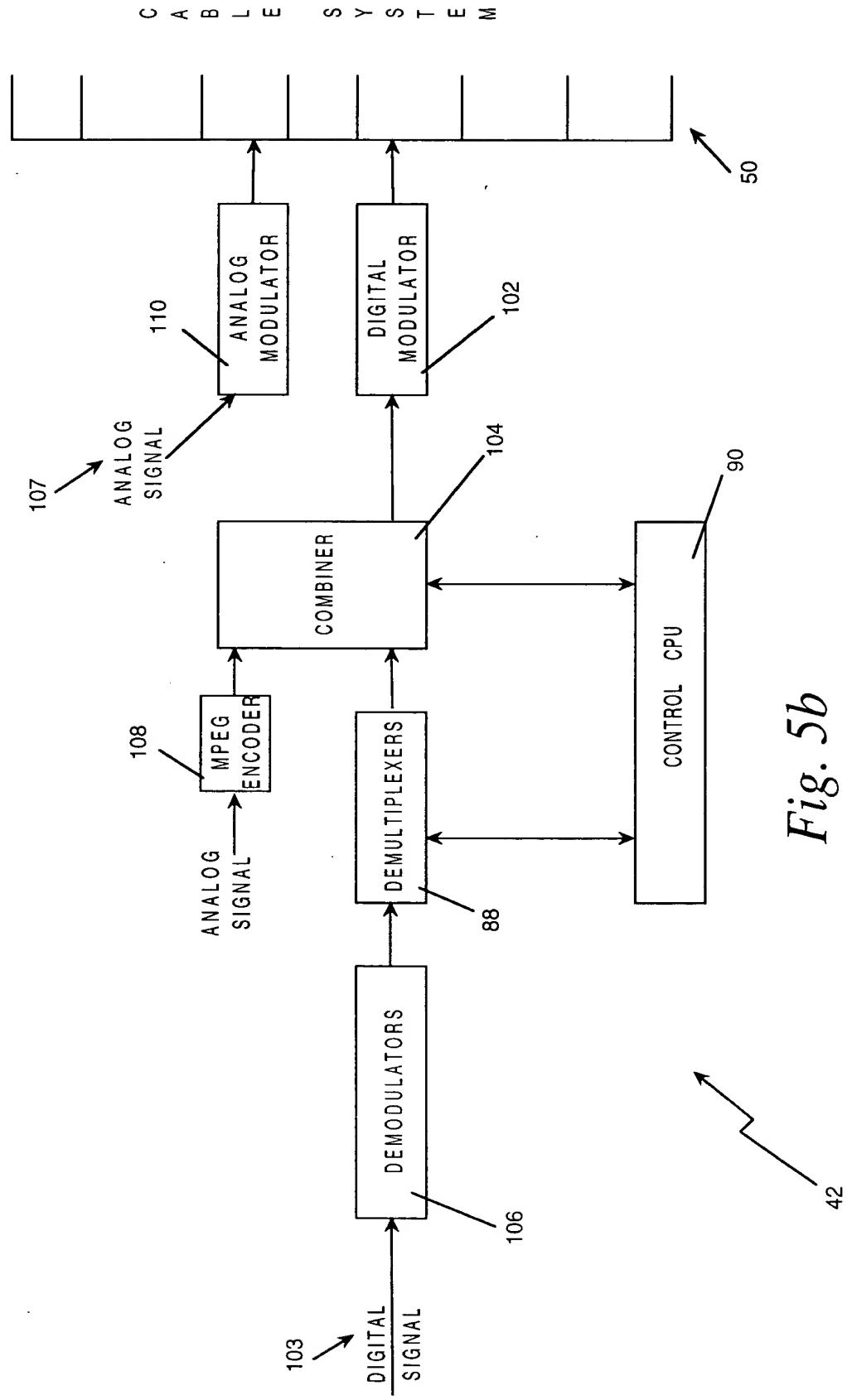


Fig. 5b

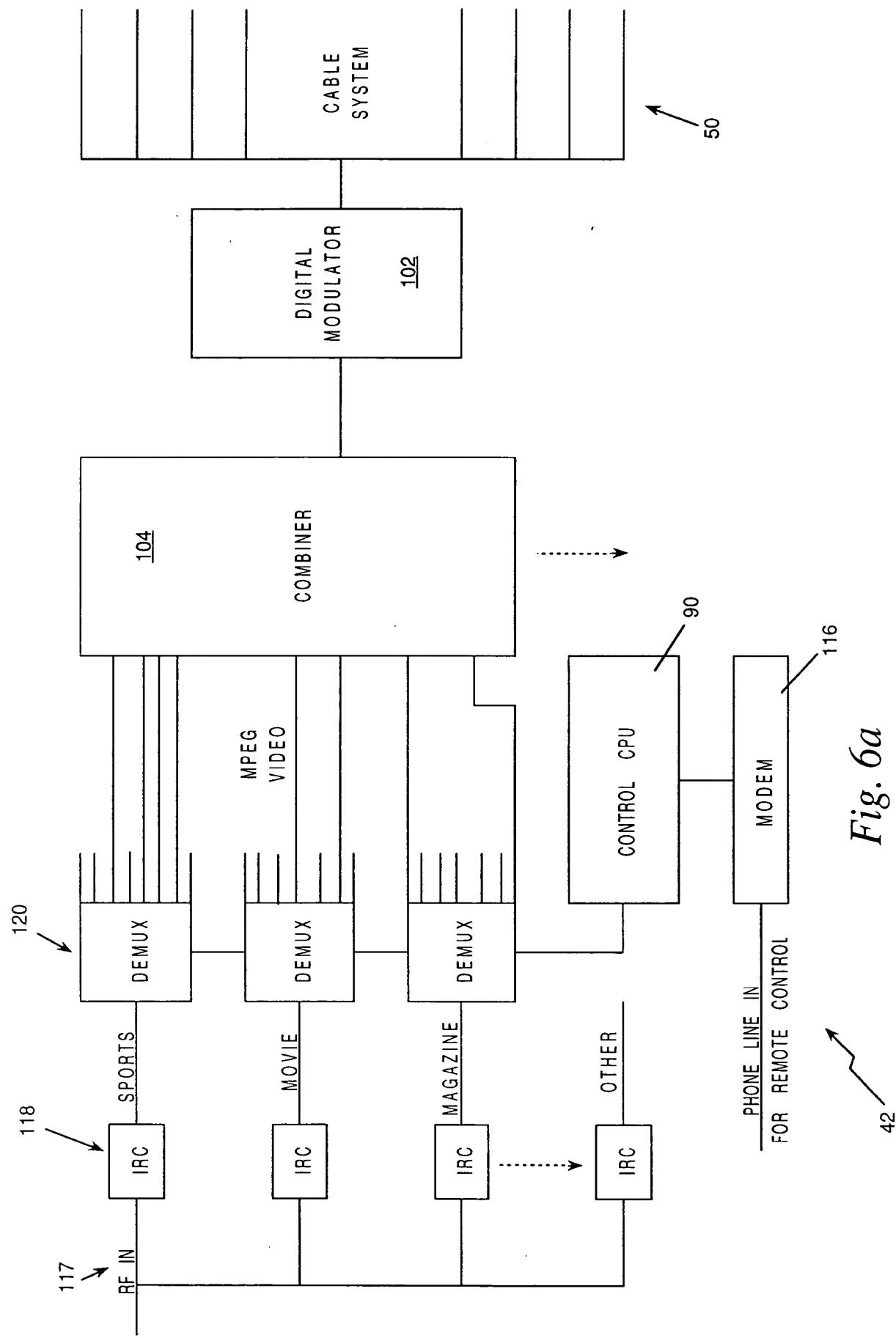
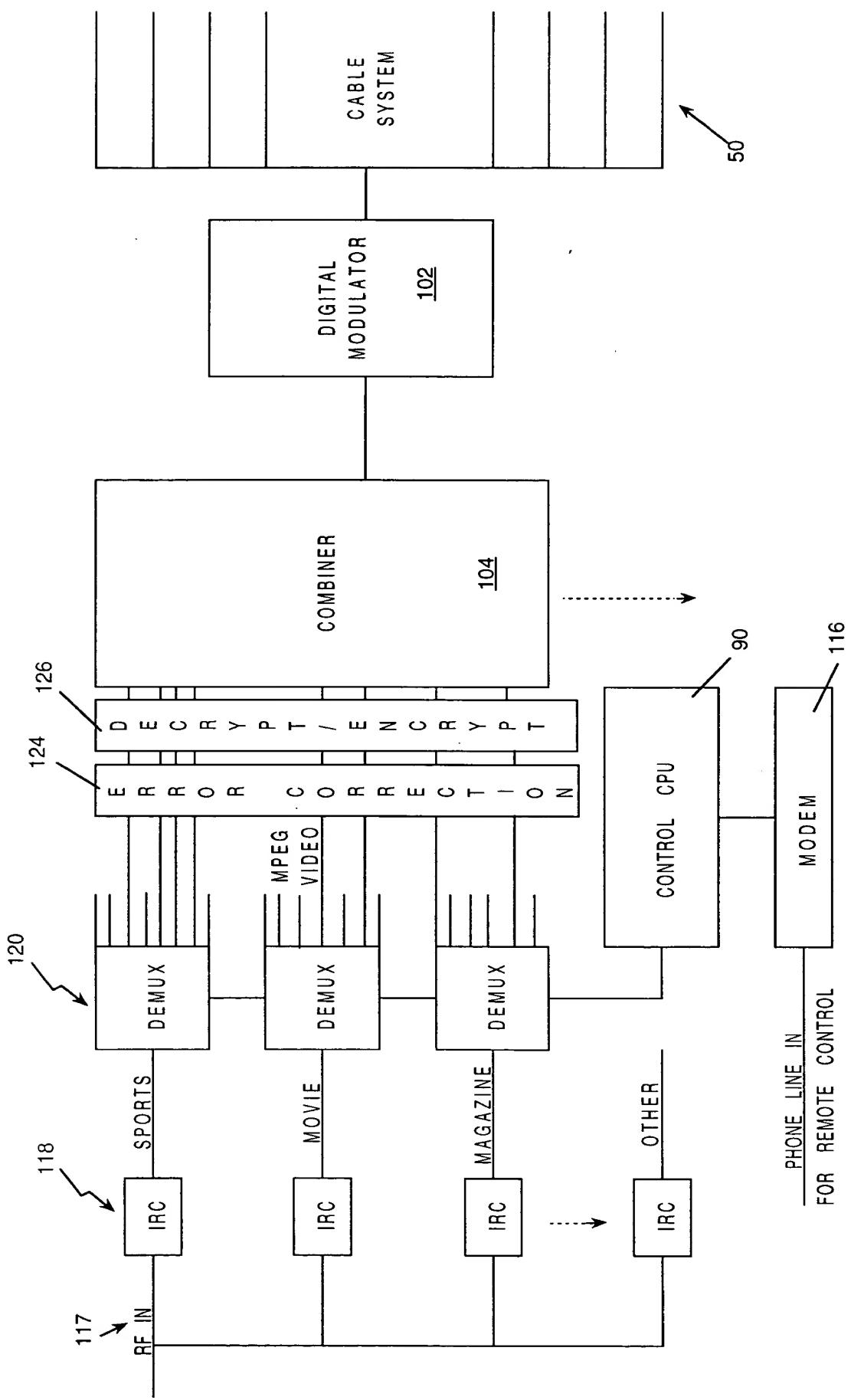


Fig. 6a



*Fig. 6b*

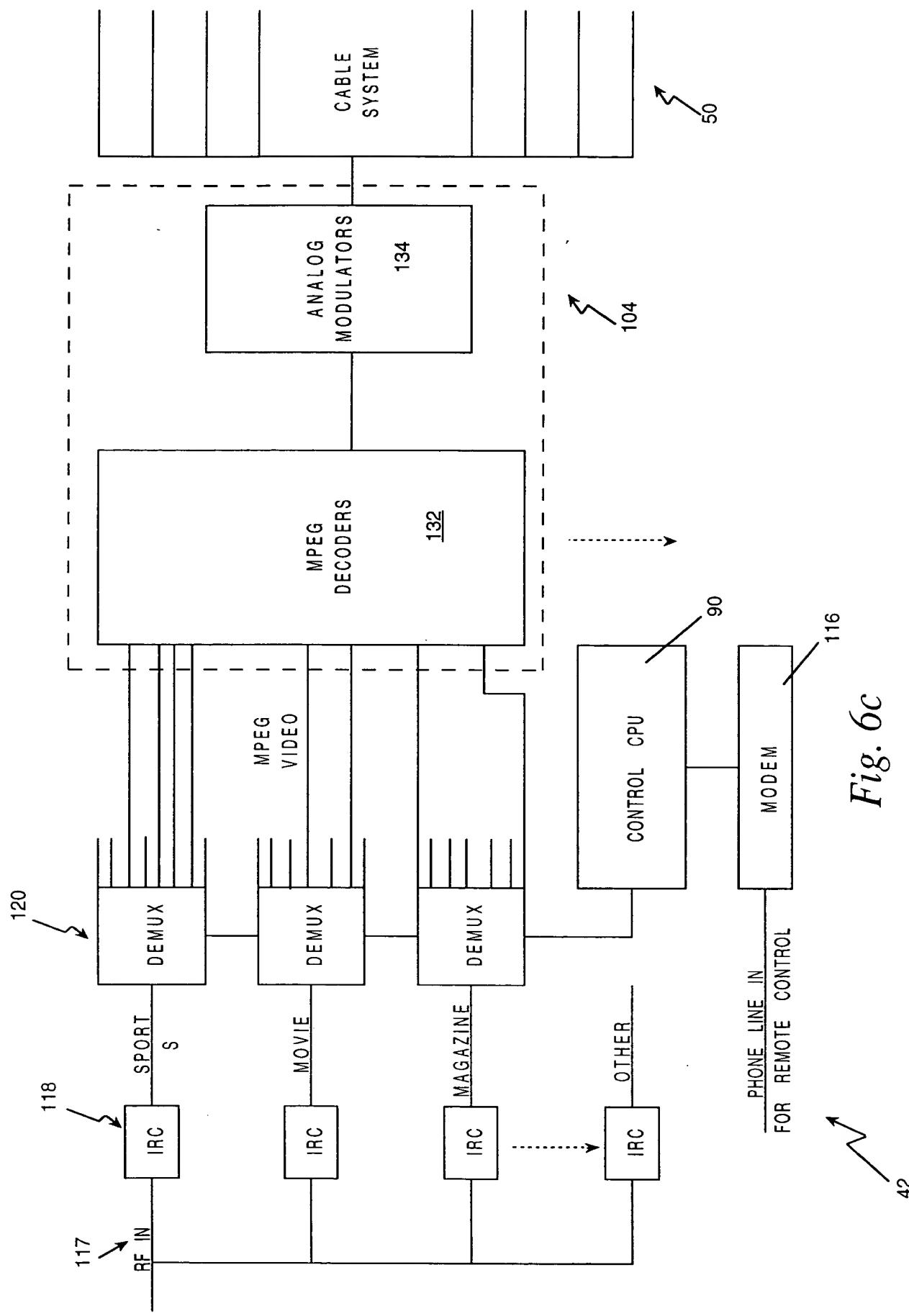
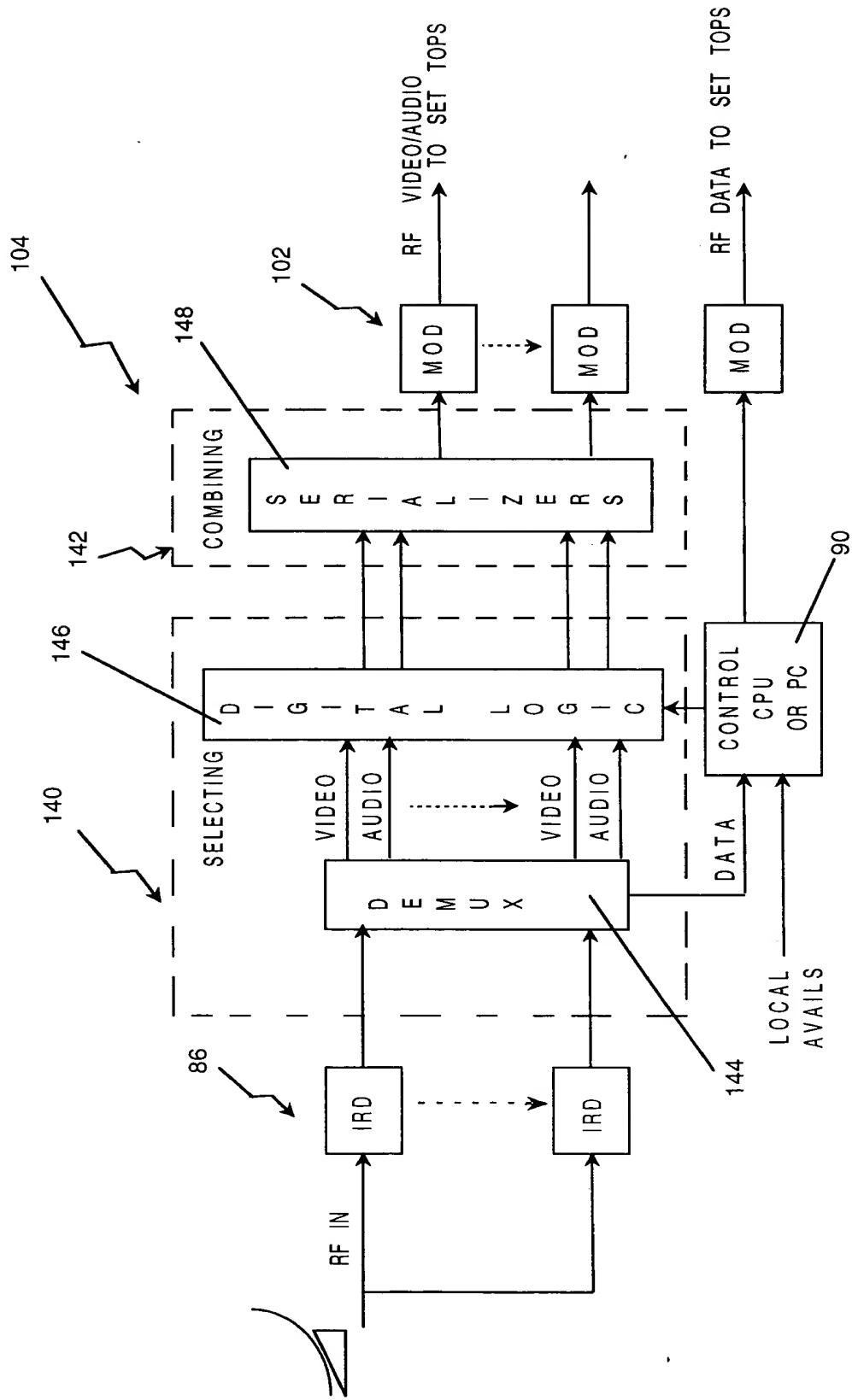
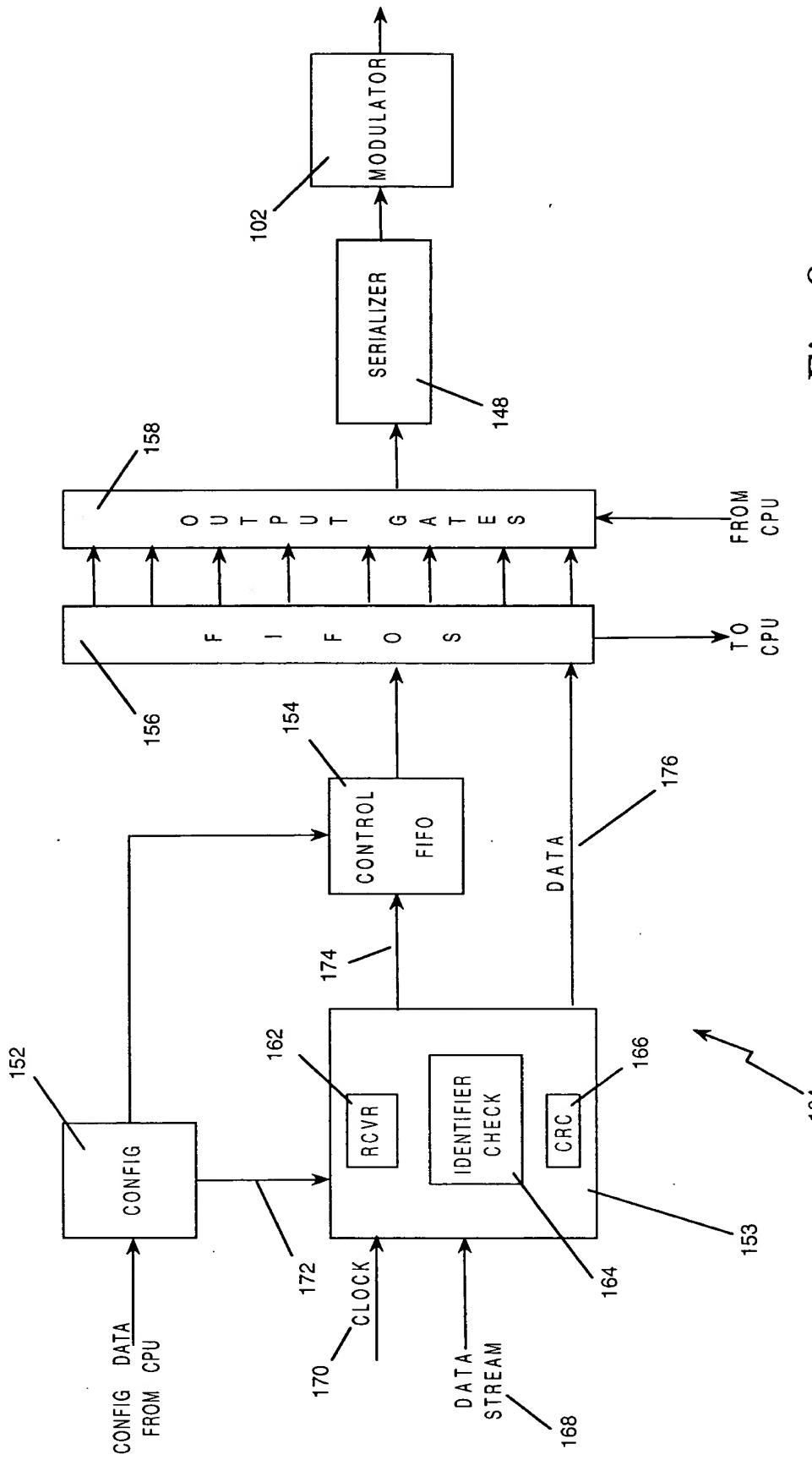


Fig. 6c

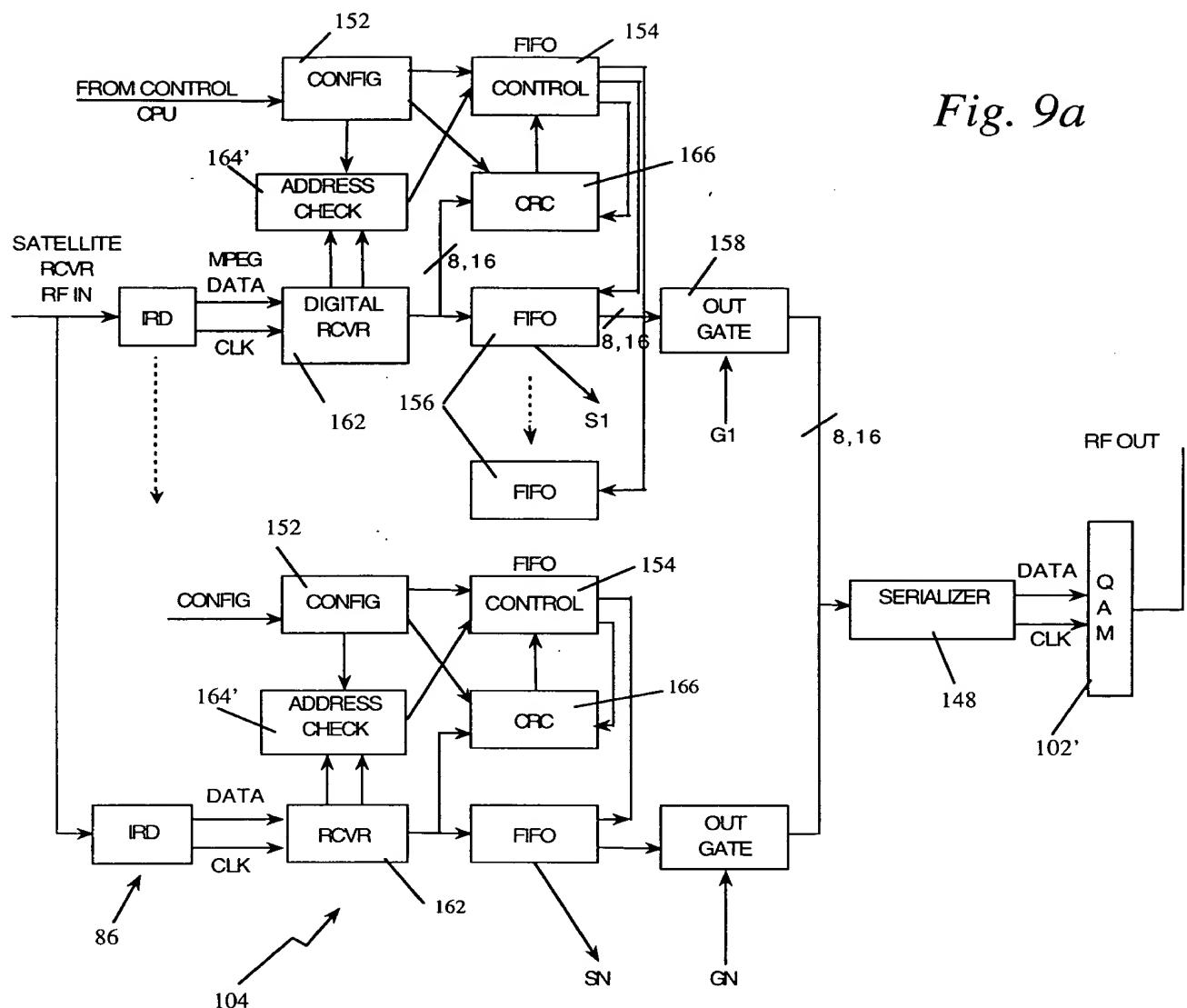
*Fig. 7*

DIGITAL CABLE HEADEND WITH COMBINER

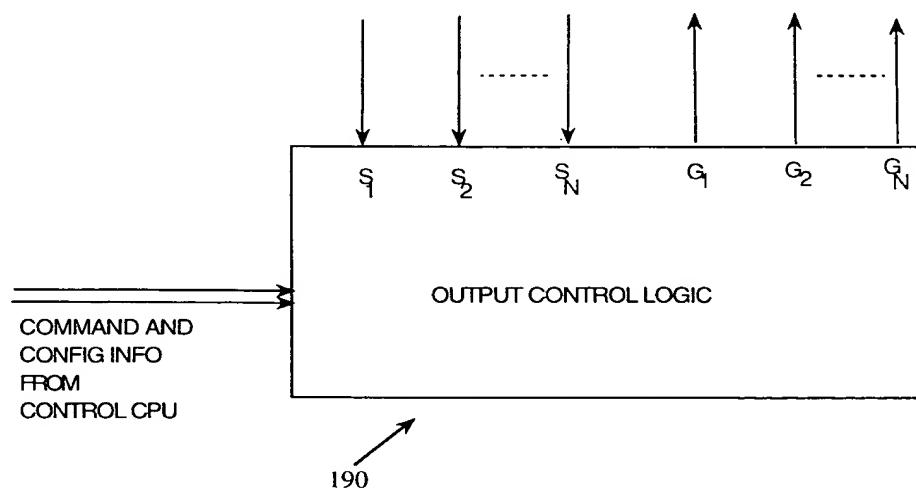
*Fig. 8*

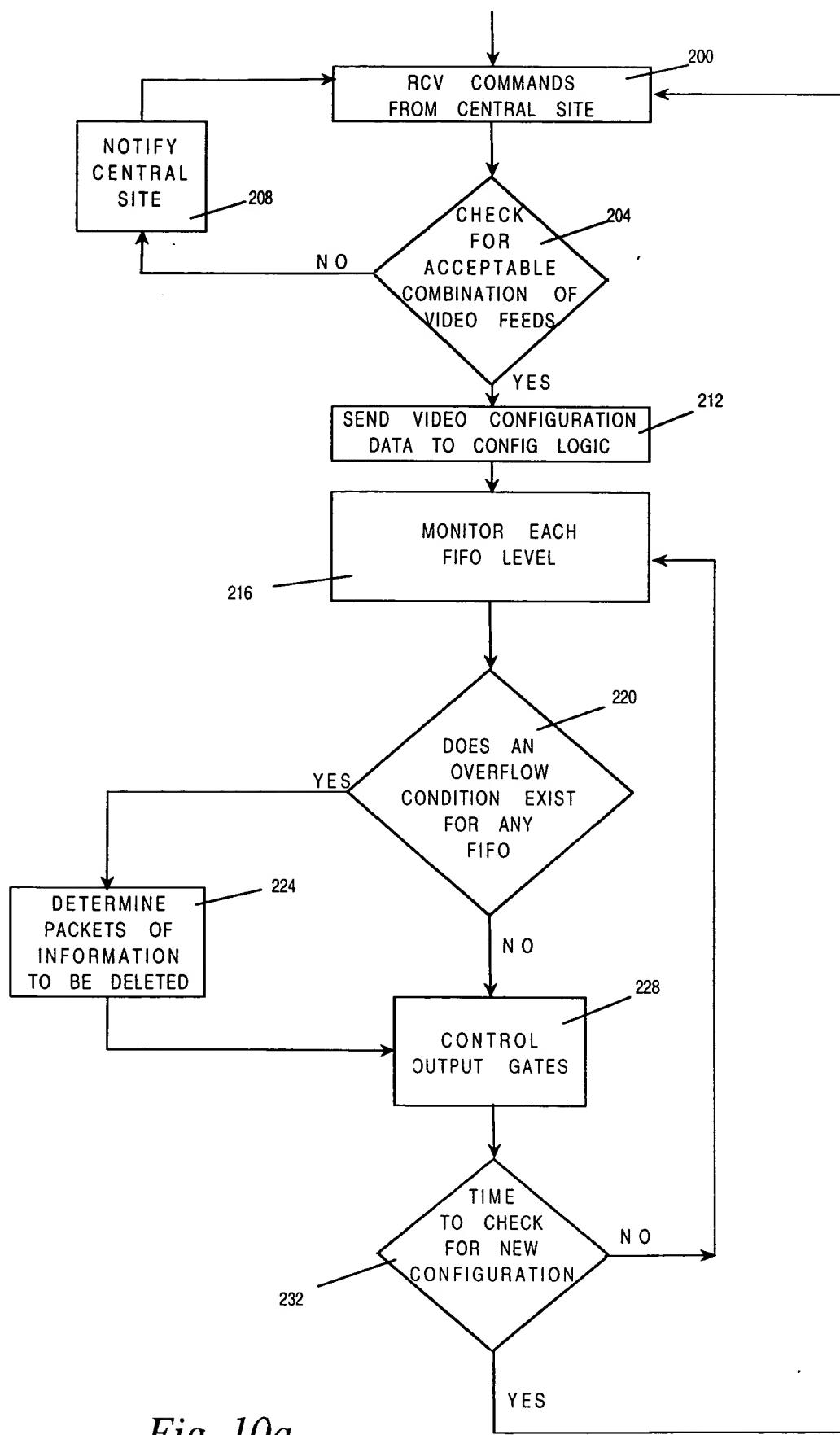


*Fig. 9a*



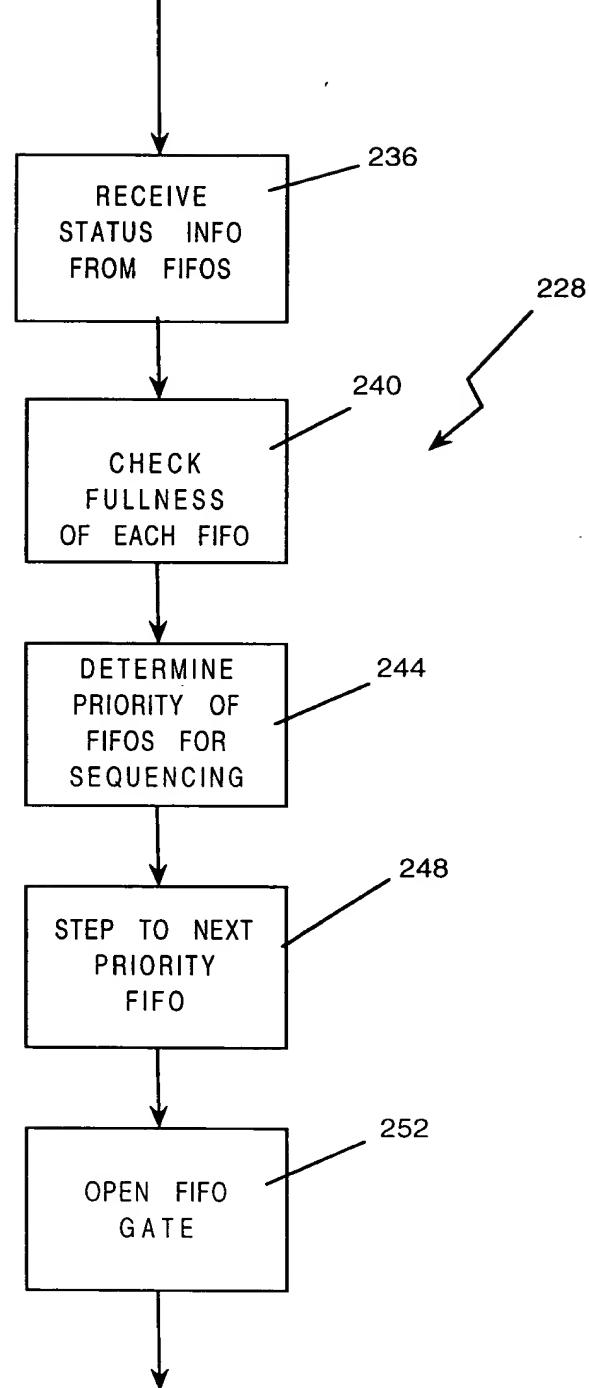
*Fig. 9b*



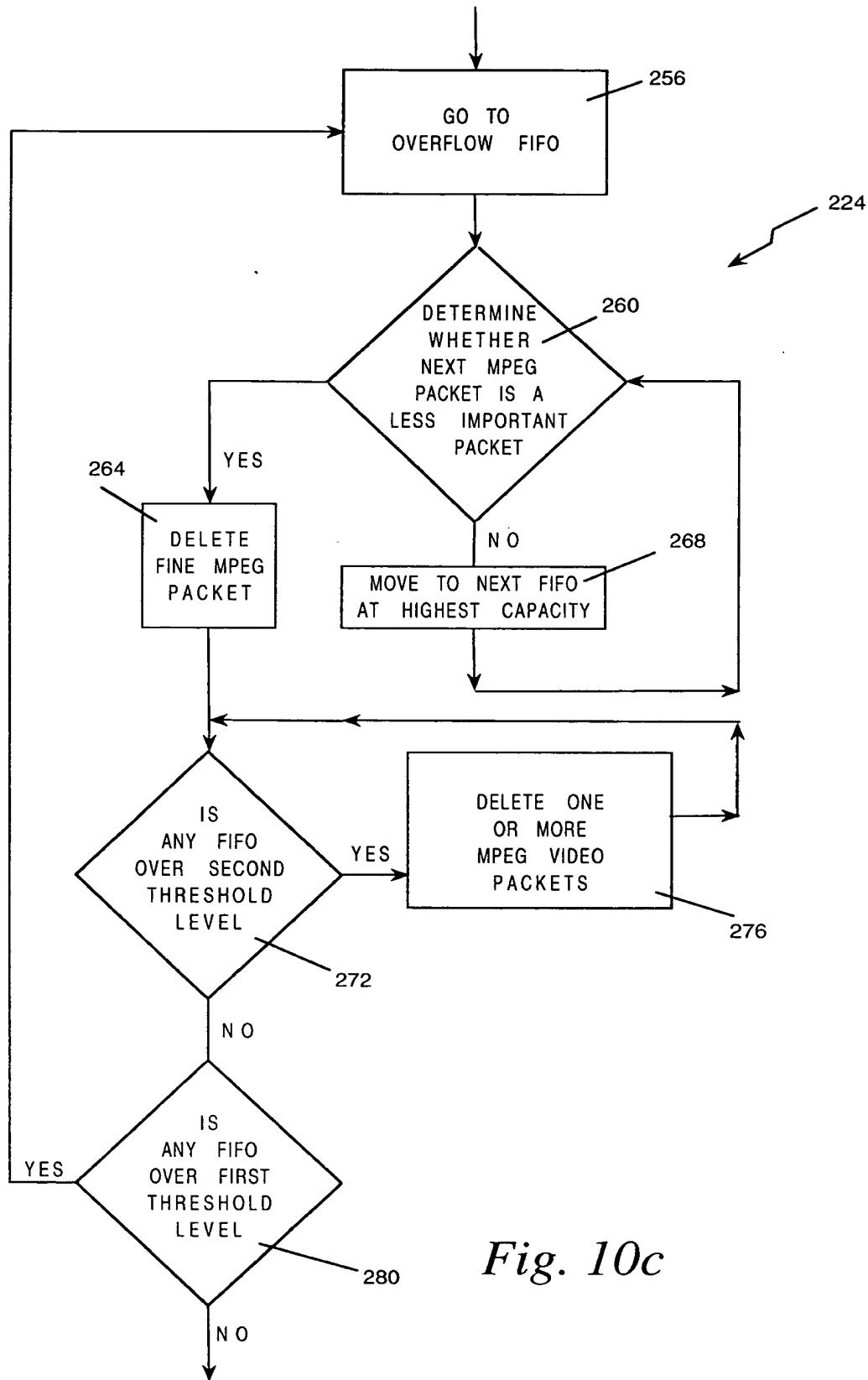


*Fig. 10a*

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*Fig. 10b*



*Fig. 10c*

*Fig.11*

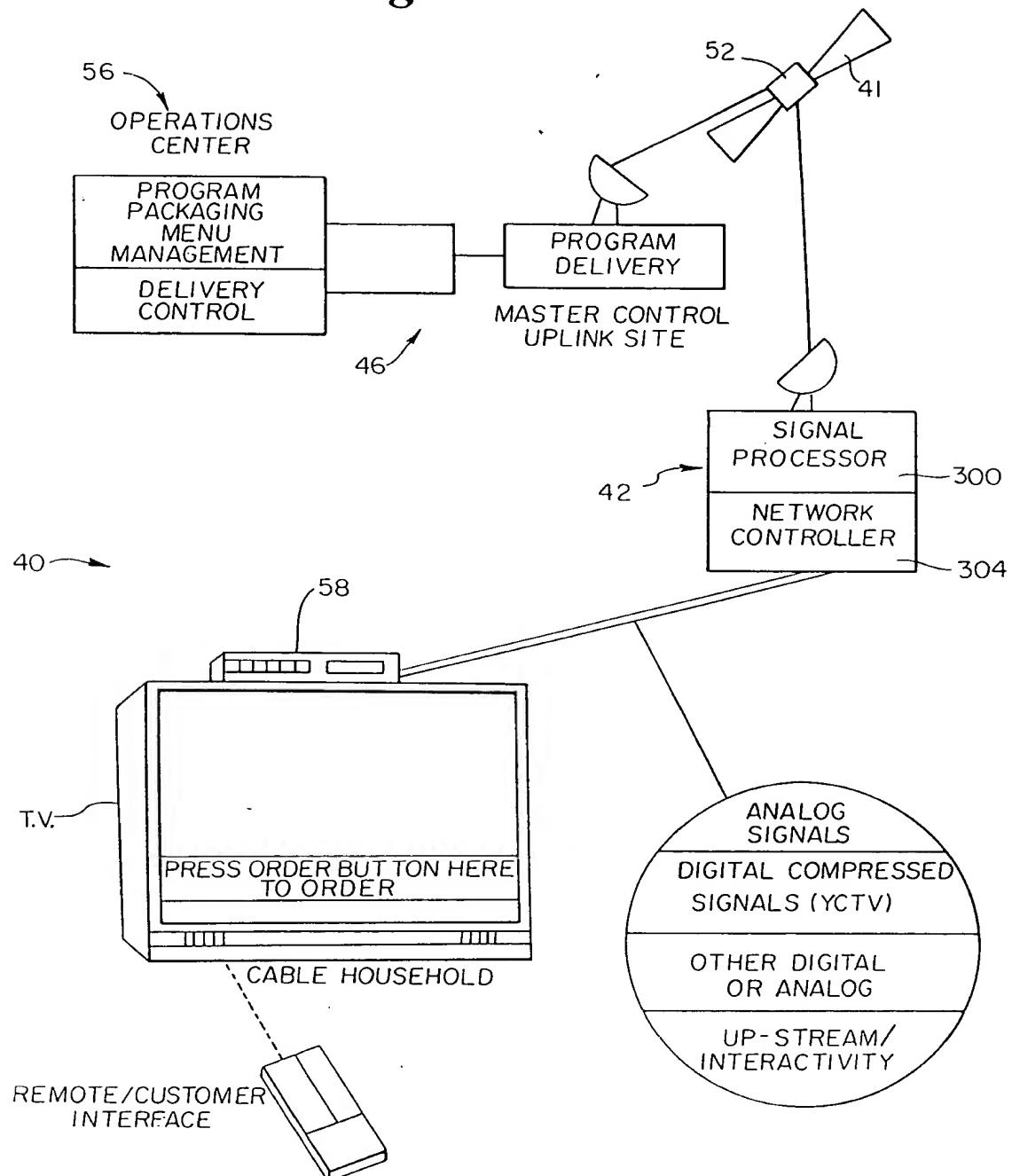


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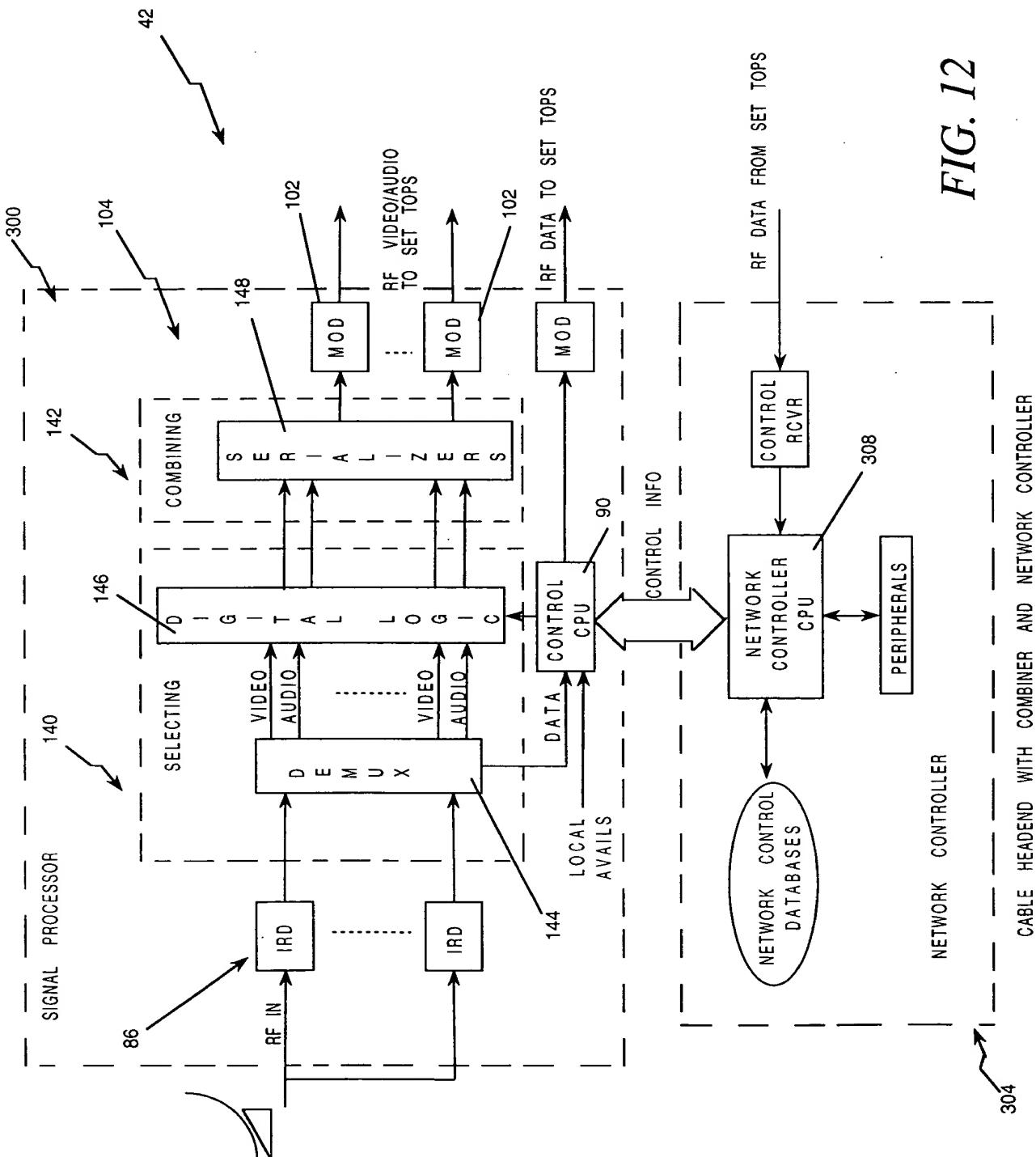


FIG. 12